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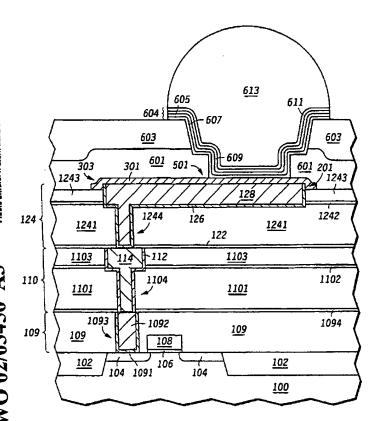
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(54) Title: SEMICONDUCTOR DEVICE AND METHOD OF FORMATION



(57) Abstract: A semiconductor device and its method of formation are disclosed wherein a surface of a semiconductor substrate is planarized to form an interconnect (1244) within a dielectric layer (1243). The top surface of the dielectric layer is then recessed with respect to a top surface of the interconnect to form a step (201). An opaque film (301) is then deposited over the surface of the semiconductor substrate. The opaque film (301) is lithographically patterned and etched, wherein an alignment of the patterning layer (401) is accomplished using topographically discernable features (303) that are formed in the opaque film (301) in regions where the step (201) between the interconnect (1244) and dielectric layer (1243) is produced.

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PCI/US 01/20947 A. CLASSIFICATION OF SUBJECT MATTER IPC 7 H01L21/768 H01L H01L21/60 According to International Patent Classification (IPC) or to both national classification and IPC **B. FIELDS SEARCHED** Minimum documentation searched (classification system tollowed by classification symbols) IPC 7 HO1L Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched Electronic data base consulted during the international search (name of data base and, where practical search terms used) EPO-Internal, WPI Data, PAJ C. DOCUMENTS CONSIDERED TO BE RELEVANT Relevant to claim No. Citation of document, with indication, where appropriate, of the relevant passages 1,5 US 5 863 825 A (OSUGI RICHARD ET AL) χ 26 January 1999 (1999-01-26) column 2, line 42 -column 3, line 14 column 3, line 48 -column 4, line 5; figures 2,5,6 WO 99 08314 A (OKAMOTO YOSHIHIKO ; HITACHI χ LTD (JP)) 18 February 1999 (1999-02-18) 2-4,6-10 A abstract page 20, line 24 -page 22, line 16 page 23, line 22 -page 24, line 13 page 25, line 10 -page 26, line 22 page 33, line 23 -page 36, line 9 page 40, line 24 -page 45, line 17 page 52, line 22 -page 54, line 14; figures 9-13,18-21,23-26,33-41,48-58,76-81 Patent family members are listed in annex. Further documents are fisted in the continuation of box C. Special categories of cited documents: "T" tater document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the *A* document defining the general state of the art which is not considered to be of particular relevance 'E' earlier document but published on or after the international "X" document of particular relevance; the claimed Invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone "L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified) "Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the off "O" document referring to an oral disclosure, use, exhibition or document published prior to the international filing date but later than the priority date claimed *&* document member of the same patent family Date of the actual completion of the international search Date of mailing of the international search report 15/04/2002 8 April 2002 Name and mailing address of the ISA Authorized officer European Patent Office, P.B. 5818 Patentiaan 2

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